PAGE 1/12 \* RCVD AT 2/2/2004 2:26:52 PM [Eastern Standard Time] \* 5VR:USPTO-EFXRF-1/0 \* DMIS:8729306 \* CSID:518 449 0047 \* DURATION (mm-ss):01-04

T AND TRADEMARK OFFICE

Serial No.:

10/063,225

FEB 1 9 2004

Conf. No.:

9511

FEB 0 4 2004

Filing Date:

04/01/2002

OFFICE OF PETITIONS

Art Unit:

2837

Salata, Anthony J.

Applicant:

Voldman

Examiner:

Title:

DUAL EMITTER TRANSISTOR WITH

Docket No.: BUR920010117

**ESD PROTECTION** 

## COMMISSIONER FOR PATENTS

**DESTINATION PHONE NUMBER:** 

703-872-9306

Transmitted herewith is:

Comments on Statement of Reasons for Allowance in 1 page

in the above identified application.

No additional fee is required.

The Commissioner is hereby authorized to charge and credit Deposit Account No. 500999 as described below.

Credit any overpayment.

Charge any additional fee required.

DATE: February 2, 2004

## CERTIFICATION OF FACSIMILE TRANSMISSION

I heroby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date shown below.

John LaBatt (Person transmitting this correspondence)

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

This paper is being filed in response to the Notice of Allowability dated December 29, 2003. On page 2 of the Notice, in paragraph 1 of the Examiner's comments, the Examiner indicates that claims 1-10 are allowed. It is Applicant's understanding that all pending claims, i.e., claims 1-20, are allowed as indicated on the first page of the Notice. Should the Examiner require anything further, the Examiner is invited to contact Applicant's undersigned representative at the number listed below.

Respectfully submitted,

John W. LaBatt, Reg. No. 48,301

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Serial No. 10/063,225

Page 1 of 1

Dated: February 2, 2004